

ADDENDUM • OPEN ACCESS

Addendum: Accessing register spaces in FPGAs within the ATLAS DAQ scheme via the SCA eXtension

To cite this article: C. Bakalis *et al* 2021 *JINST* **16** A11001

View the [article online](#) for updates and enhancements.



The Electrochemical Society
Advancing solid state & electrochemical science & technology

241st ECS Meeting

May 29 – June 2, 2022 Vancouver • BC • Canada

Abstract submission deadline: Dec 3, 2021

Connect. Engage. Champion. Empower. Accelerate.
We move science forward



Submit your abstract



Addendum: Accessing register spaces in FPGAs within the ATLAS DAQ scheme via the SCA eXtension

C. Bakalis,^{a,b,1,*} T. Alexopoulos,^a I. Grayzman,^c L. Lee Jr.,^d L. Levinson,^c M. Perganti^a and V. Polychronakos^b

^aNational Technical University of Athens, Athens 15773, Greece

^bBrookhaven National Laboratory, Upton NY 11973, U.S.A.

^cWeizmann Institute of Science, Rehovot 7610001, Israel

^dHarvard University, Cambridge MA 02138, U.S.A.

E-mail: cbakalis@lbl.gov

ADDENDUM TO: [2021 JINST 16 P06041](#)

In this addendum we added the author “M. Perganti”.

The correct authorlist is:

C. Bakalis, T. Alexopoulos, I. Grayzman, L. Lee Jr., L. Levinson, M. Perganti, V. Polychronakos.

Acknowledgments

This research is co-financed by Greece and the European Union (European Social Fund-ESF) through the Operational Programme “Human Resources Development, Education and Lifelong Learning 2014–2020” in the context of the project “Develop of an advanced system for evaluating of the data readout electronics system in Micromegas detectors for the upgrade of the ATLAS experiment” (MIS 5049537).

¹Currently with Lawrence Berkeley National Laboratory, Berkeley CA 94720, U.S.A.

*Corresponding author.