

# Energy band profile of Al /HfO, / p-Ge MOS structures by **XPS and electrical characterization**

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## ABSTRACT

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The construction of the energy band profile of MOS structures based on high-k dielectrics and high mobility substrates is critical for the understanding and optimization of their electrical response [1,2]. It is well known that some of the crucial criteria for an alternative to SiO<sub>2</sub> gate dielectric include a defectless interface (atomically flat with low rms roughness) exhibiting low Density of interface traps (D<sub>it</sub>), a high value of permittivity (at least higher than SiO<sub>2</sub>) as well as a sufficient barrier height that will effectively block electrons and holes. High quality HfO<sub>2</sub> constitutes one of the possible alternative high-k dielectrics mainly due to its chemical stability and high permittivity value (k=20-25) [3,4]. The aim of the present work is the construction of the interface band diagrams of HfO<sub>2</sub>/ p-Ge combining two different experimental techniques i.e. X-ray Photoelectron Spectroscopy and electrical conductivity measurements. The implementation of these two methods gives a thorough insight of the electrical behaviour of MOS structures. HfO<sub>2</sub>/ p-Ge, up to 15nm, structures were grown via Atomic Layer Deposition (ALD) technique at 250°C. The precursor, used for the deposition of HfO<sub>2</sub> was Tetrakis(Dymethylamido)Hafnium while the co-reactant / oxidant was water. The chemical composition as well as the energy barrier height between 5nm HfO<sub>2</sub> / p-Ge interface were studied ex situ by XPS. Al gate electrodes were deposited onto HfO<sub>2</sub> films, by thermal evaporation technique, resulting in Al / 15nm HfO<sub>2</sub>/ p-Ge MOS structures. The structures were electrically characterized through the analysis of C-V, G-V and C-f measurements in order to evaluate the density of interfacial traps (D<sub>it</sub>) and the EOT value. Furthermore, J-V measurements were performed and analyzed, for both positive and negative gate voltages with temperature as a parameter. Conduction mechanisms and energy barrier heights were verified / calculated for both negative and

# **EXPERIMENTAL**

positive electric fields.

## (a) Sample preparation

- > 5nm and 15nm thin films of HfO<sub>2</sub> grown by ALD onto p-Ge substrates.
- >The substrates where properly cleaned prior to the deposition (HF 2% v/v and DI).
- >Thin films were deposited by Atomic Layer Deposition, ALD (Savannah-100 ALD system) by Cambridge-Nanotech/USA), through 160 alternating pulses of  $H_2O$  and Tetrakis (Dymethylamido) Hafnium The deposition temperature was 250°C and the average growth rate of the films was 0.94 Å/cycle.

# (b) Chemical & Electrical characterization

# $\succ$ XPS analysis.

- The 5nm HfO<sub>2</sub>/Ge interfaced samples were introduced in Ultra High Vacuum and characterized by XPS (non-monochromatized AlK $\alpha$  X-Ray source - 1486.6 eV-).
- Photoemission spectra were collected by a Leybold-Heraeus EA11 hemispherical energy analyser operating at constant pass energy of 100 eV.
- Adventitious carbon at 284.8 eV was used as a reference for electrostatic charging correction.
- > Formation of ~300nm top Al electrode by thermal evaporation. Patterned capacitors with gate area (4x4)mm<sup>2</sup>.

➢ I-V characteristics by Keithley 2611A in the temperature range from 163K to 393K.

# **CHEMICAL CHARACTERIZATION**



# **ELECTRICAL CHARACTERIZATION**

# **I-V CHARACTERISTICS**



Figure 3: (a) I-V characteristics for Al/15nm HfO<sub>2</sub>/ p-Ge under substrate (positive voltages) and gate electron injection (negative voltages) in the temperature range from 163K to 393K.



Figure 4: lnJ – lnE characteristics for gate voltages lower than 0.2 V in the temperature range between 253K and 393K for (a) substrate electron injection and (b) gate electron injection. Insets: Arrhenius plot for low (i) positive and (ii) negative voltages in the same



### **VOLTAGE REGION I (V<0.2V)**

- > In the low voltage region at temperatures higher than 253K, the prevailing conduction mechanism both for substrate (positive voltages) and gate electron injection (negative voltages) is Ohmic (Fig. 4a,b).
- ≻ The activation energy of the Ohmic conduction mechanism, in both cases, is evaluated, according to eq. 1 [8],

$$J = \sigma E e^{\frac{-E_{act}}{KT}} \quad (1)$$

and equals to 0.28 eV (inset of fig.4a,b).

### VOLTAGE REGION II (0.6V<V<sub>app</sub><1.4V)

≻ For positive, higher than 0.6V, applied voltages in the temperature range between 163K and 313K and for negative voltages between  $0.6V < V_{app} < 1.4V$  in the whole temperature range, the conduction is dominated by the Poole Frenkel mechanism.

$$J = q \mu N_c e^{\frac{-q(\varphi_{\tau} - \sqrt{qE/\pi\varepsilon\varepsilon_0})}{2K_B T}}$$
(2)

≻The activation energy is calculated to be equal to 0.37 eV (inset of fig. 5a,b) [9].

# VOLTAGE REGION III (V<sub>app</sub>>1,4V)

▶ For temperatures higher than 313K, in the case of positive applied voltages, Schottky is the prevailing conduction mechanism.

$$J = A^* T^2 e^{\frac{-q(\varphi_{\rm B} - \sqrt{qE/4\pi\varepsilon\varepsilon_0})}{K_{\rm B}T}} \quad (3)$$

> Fitting the experimental data to eq. 3 (fig. 6a), the barrier height ( $\phi_B$ ) between HfO<sub>2</sub> / p-Ge, is evaluated to be equal to 1.5 eV.

➢ For negative voltages the dominant conduction mechanism is the trap controlled space charge limited (TSCLC) (fig. 6b)

$$J = \frac{8\mu\varepsilon_i}{9d^3} V^{\text{A}} \text{ where A>2 (4)}$$

with A=1+ $\sigma/kT$  and  $\sigma$ : the trap energy dispersion [10].

 $\triangleright$  The value of  $\sigma$  was calculated to be 0.1eV (inset of fig. 7a) indicating shallow traps, located near the coduction band [11].

Calculated, through XPS and I-V analysis, values of  $\phi_{\rm B}$  differ of about 0.4eV.

> I-V measurements in a broader voltage and temperature range, have to be performed in order to get a better view of Schottky conduction mechanism and the evaluation of

#### > The values pointed out in black are values taken from the literature.

I-V measurements.



Valence

conduction band offset.

Figure 7: energy band diagram is constructed combining XPS and electrical characterization experimental data

**Valence Band Offset** 

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# CONCLUSIONS

 $\clubsuit$  The stoichiometry of HfO<sub>2</sub> deposited films has been identified by XPS.

\* The valence band maxima of Bulk HfO<sub>2</sub> and Bulk Ge, as detected by XPS, appear at  $2.7 \pm 0.1$  eV and at  $0.0 \pm 0.1$  eV respectively.

✤ I-V analysis revealed that:

- For both low positive and low negative applied voltages, Ohmic is the prevailing mechanism with an activation energy of 0.28 eV.
- For positive applied voltages higher than 0.6V (i) in the temperature range between 163 and 313K, Poole Frenkel is the dominant conduction mechanism with activation energy equal to 0.37eV, while (ii) for higher temperatures, Schottky conduction mechanism is the prevailing one. The barrier height of  $HfO_2$  / p-Ge is evaluated to be 1.5V.
- For negative applied voltages (i) in the range of 0.6V<V<sub>app</sub><1.4V, the prevailing conduction mechanism is Poole-Frenkel with an activation energy of 0.37eV, while (ii) for voltages higher than 1.4V the trap controlled space charge limited conduction is the dominant one.
- \* The energy band diagram is constructed combining XPS and electrical characterization experimental data. The calculated barrier heights from the I-V (1.5 eV) and XPS (1.9 eV) analysis, are in fair agreement.